ImageWriter

MicroChip PIC16F872 Device Support Detail & Setup

ImageWriter Support Hierarchy

Manufacturer: MicroChip

Device: PIC16F872

Objects for PIC16F872

Flow Object Name:	*Program / Execute*	*Verify*	*Load*	*Object Size (words)*
FLASH	●	●	●	0x800
EEPROM[1]	●	●	●	0x40
USER ID	●	●	●	0x04
CONFIG	●	●	●	0x01
DEVICE ID			●	0x01
ERASE EEPROM	●			
CHIP ERASE	●			
p_MCT16F[2]	●		●	0x08 (bytes)

[1] EEPROM Data is LSB byte data stored in 16-bit words. MSB is read as device dependent don't cares (usually 0x3F or 0x00). Verify operations use entire 16-bit word.
[2] Default Clock Speed = 500KHz

I/O Multi Connector Setup for PIC16F872
High Voltage ICSP Method

ImageWriter:		Target:
Connector Pin #	***Pin Name***	***Connections***
1	GND	GND
2	GND	GND
3[1]	TXD (MOSI/TDO)	See note 1
4[1]	RXD (MISO/TDI)	RB7
5	RTS (TMS)	NOCONN
6	CTS	NOCONN
7	XCLK	RB6
8	RST	NOCONN
9	SCL	NOCONN
10	SDA	NOCONN
11	GND	GND
12	GND	GND
13	VPP/EXT_IN	13.0 V DC
14	VPP/EXT_OUT	MCLR
15*	Target V+	Supply 2.2-5.5 V
16*	Target V+	Supply 2.2-5.5 V
17	GND	GND
18	GND	GND
19	Pod Power	Supply 9-24 VDC
20	Pod Power	Supply 9-24 VDC

[1] Pin 3 must be connect to Pin 4 with an external 1K Ohm resistor in series.

Support Notes:

1. Onboard programming is supported via both Low and High Voltage ISCP methods. Use only one of the ImageWriter Pin-connection's methods above to connect ImageWriter to your target board.
2. Data verification does not occur automatically during program. Include a Verify operation in your flow to ensure data integrity (highly recommended).
3. Fuse bit mapping is per manufacturer data-sheet. All bits stored in ImageWriter memory must be properly set.
4. The Chip Erase object will erase code protected devices and remove the code protected status.
5. The Erase EE object only functions on unprotected EEPROM.

I/O Multi Connector Setup for PIC16F877A
Low Voltage ICSP Method

ImageWriter:		Target:
Connector Pin #	***Pin Name***	***Connections***
1	GND	GND
2	GND	GND
3[1]	TXD (MOSI/TDO)	See note 1
4[1]	RXD (MISO/TDI)	RB7
5	RTS (TMS)	RB3
6	CTS	NOCONN
7	XCLK	RB6
8	RST	MCLR
9	SCL	NOCONN
10	SDA	NOCONN
11	GND	GND
12	GND	GND
13	VPP/EXT_IN	NOCONN
14	VPP/EXT_OUT	NOCONN
15*	Target V+	Supply 2.2-5.5 V
16*	Target V+	Supply 2.2-5.5 V
17	GND	GND
18	GND	GND
19	Pod Power	Supply 9-24 VDC
20	Pod Power	Supply 9-24 VDC

[1] Pin 3 must be connect to Pin 4 with an external 1K Ohm resistor in series.

Support Notes:

1. Onboard programming is supported via both Low and High Voltage ISCP methods. Use only one of the ImageWriter Pin-connection's methods above to connect ImageWriter to your target board.
2. Data verification does not occur automatically during program. Include a Verify operation in your flow to ensure data integrity (highly recommended).
3. Fuse bit mapping is per manufacturer data-sheet. All bits stored in ImageWriter memory must be properly set.

4. The Chip Erase object will erase code protected devices and remove the code protected status.
5. The Erase EE object only functions on unprotected EEPROM.

Disclaimer

Typical implementation of **ImageWriter** includes but is not limited to external power provision to **ImageWriter**, device access restrictions on the target board, external hardware on the target board, or software changes created by the user affecting timing, device erasure, programming, or verification. Data I/O warranty coverage applies only when the product is used in conformance with technical specifications put forth by Data I/O and by the semiconductor device manufacturer.

In the interest of achieving the highest levels of safety and quality, Data I/O recommends that all programming processes be tested and documented in a prototype project prior to transition to volume manufacturing. For additional information see In-System Programming Design Guidelines (http://www.dataio.com/isp).

ImageWriter programs semiconductor devices through the use of circuits designed precisely to operate in accordance with specifications outlined by the device manufacturer. Provision and calibration of power supplies are critical for proper operation. **ImageWriter** has been fully tested by Data I/O Corporation for quality performance.